



## RF Design Engineers

### Requirements:

- ✓ University degree (Licenciatura or equivalent) in Electronic Engineering
- ✓ Minimum 3 years design experience (academic work can be considered) in any given RF discipline (LNAs, Mixers, VCOs, PLLs, Filters, are highly desirable)
- ✓ Practical design work and knowledge of simulation tools and layout techniques
- ✓ Knowledge of transceiver architectures (direct conversion, super het, etc.)

### Preferential Requirements:

- ✓ Good knowledge of the Cadence framework

### Duties:

- ✓ Development of RF analog cells in CMOS on Cadence design platform
- ✓ Responsible for all aspects of development including design, simulation and layout

Please send your application, resume and motivation to: [jobs.taguspark@chipidea.com](mailto:jobs.taguspark@chipidea.com) or to [careers@chipidea.com](mailto:careers@chipidea.com).