



## **Analog Design Engineers**

### **Requirements:**

- ✓ University degree (Licenciatura or equivalent) in Electronic Engineering
- ✓ Minimum 3 years design experience (academic work can be considered) in any given analog discipline (amplifier/comparator design, data conversion, filtering, are highly desirable)
- ✓ Practical design work and knowledge of simulation tools (HSPICE preferred) and layout techniques
- ✓ Understanding of the full design cycle for integration

### **Preferential Requirements:**

- ✓ Good knowledge of the Cadence framework

### **Duties:**

- ✓ Development of analog cells and IP components in CMOS on Cadence design platform
- ✓ Responsible for all aspects of development including design, simulation and layout

Please send your application, resume and motivation to: [jobs.taguspark@chipidea.com](mailto:jobs.taguspark@chipidea.com) or to [careers@chipidea.com](mailto:careers@chipidea.com).