



Trainee Analog Design Engineer

Requirements:

- ✓ University degree or higher in Electronic Engineering
- ✓ Knowledge of CMOS analog circuit design
- ✓ Practical design work and knowledge of EDA tools (HSPICE and CADENCE platform) and layout techniques
- ✓ Good command in English
- ✓ Highly motivated, responsible and independent

Duties:

- ✓ Design of basic analog cells

Please send your application, resume and motivation to: jobs.macau@chipidea.com or to careers@chipidea.com.