



Senior Analog Design Engineer

Requirements:

- ✓ University degree or higher in Electronic Engineering (Master degree preferable)
- ✓ Minimum 5 years design experience in any given analog discipline
- ✓ Good design work and knowledge of EDA tools (HSPICE and CADENCE platform) and layout techniques
- ✓ Fluent in the full design cycle for integration
- ✓ Project management skills is highly preferable
- ✓ Good command in English and communication skills
- ✓ Highly motivated, responsible and independent

Duties:

- ✓ Development of analog and mixed-signal functional blocks and system-level IP in CMOS on Cadence design platform
- ✓ Responsible for all aspects of development including design, simulation and layout
- ✓ To lead and manage project development
- ✓ To coordinate and work as a part of a global cross-functional development team to develop mixed signal IC

Please send your application, resume and motivation to: jobs.macau@chipidea.com or to careers@chipidea.com.