



Analog Design Engineer

Requirements:

- ✓ University degree or higher in Electronic Engineering
- ✓ Minimum 3 years design experience in any given analog discipline (amplifier/comparator design, data conversion, filtering, are highly desirable)
- ✓ Practical design work and knowledge of EDA tools (HSPICE and CADENCE platform) and layout techniques
- ✓ Understanding of the full design cycle for integration
- ✓ Good command in English
- ✓ Highly motivated, responsible and independent

Duties:

- ✓ Development of analog cells and IP components in CMOS on Cadence design platform
- ✓ Responsible for all aspects of development including design, simulation and layout

Please send your application, resume and motivation to: jobs.macau@chipidea.com or to careers@chipidea.com.