



Junior Analog Design Engineer

Requirements:

- ✓ University degree in Electronic Engineering.
- ✓ Knowledge of CMOS analog circuit design.
- ✓ Knowledge of EDA tools (HSPICE and Cadence platform) preferred.
- ✓ Good communication skills in English.
- ✓ Highly motivated, responsible and willing to learn.

Duties:

- ✓ Design of basic analog cells.

Please send your application, resume and motivation to: jobs.gdansk@chipidea.com or to careers@chipidea.com.