



Analog Design Engineer

Requirements:

- ✓ University Degree or higher in Electronic Engineering
- ✓ Minimum 3 years design experience (academic work can be considered) in any given analog discipline (amplifier/comparator design, data transmitting, power management are highly desirable).
- ✓ Practical design work and knowledge of EDA tools (HSPICE preferred) and layout techniques.
- ✓ Understanding of the full design cycle for integration.
- ✓ Good communication skills in English.
- ✓ Highly motivated, responsible and independent.

Duties:

- ✓ Development of analog cells and IP components in CMOS on Cadence design platforms.
- ✓ Responsible for all aspects of development including design, simulation and layout.

Please send your application, resume and motivation to: jobs.gdansk@chipidea.com or to careers@chipidea.com.